is followed by notes, which is optional.

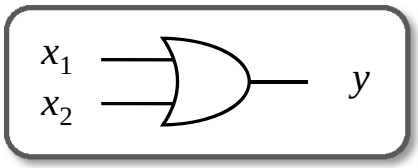
x, x_i and y are numbers

circuit description

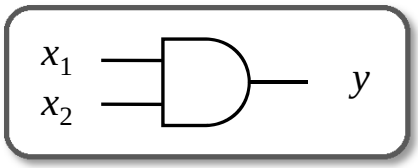
INPUT(x) # input name

OUTPUT(x) # output name

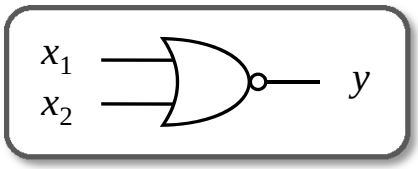
y = OR(x_1, x_2)



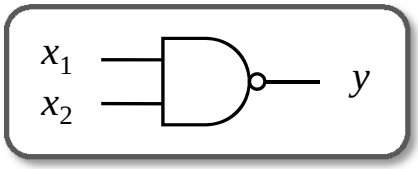
y = AND(x_1, x_2)



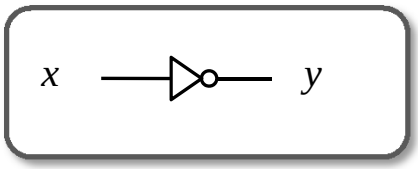
y = NOR(x_1, x_2)



y = NAND(x_1, x_2)



y = NOT(x)



Each OR, AND, NOR, NAND gate has 2 to 4 inputs.